



# DESIGN TRICKS FOR mmW/RF CHIPS

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# INCREASE OF CHANNEL BANDWIDTH

- Increase of data-rate

- Exploiting more bandwidth at higher frequencies: i.e.  $2.16 \times 4 = 8.64$  GHz on IEEE 802.11.ay



Technology for mmW?  
 DBB BW?  
 Channel bonding?

- Increasing modulation order: 64 QAM/256 QAM and beyond?



Hard requirement of LO phase noise

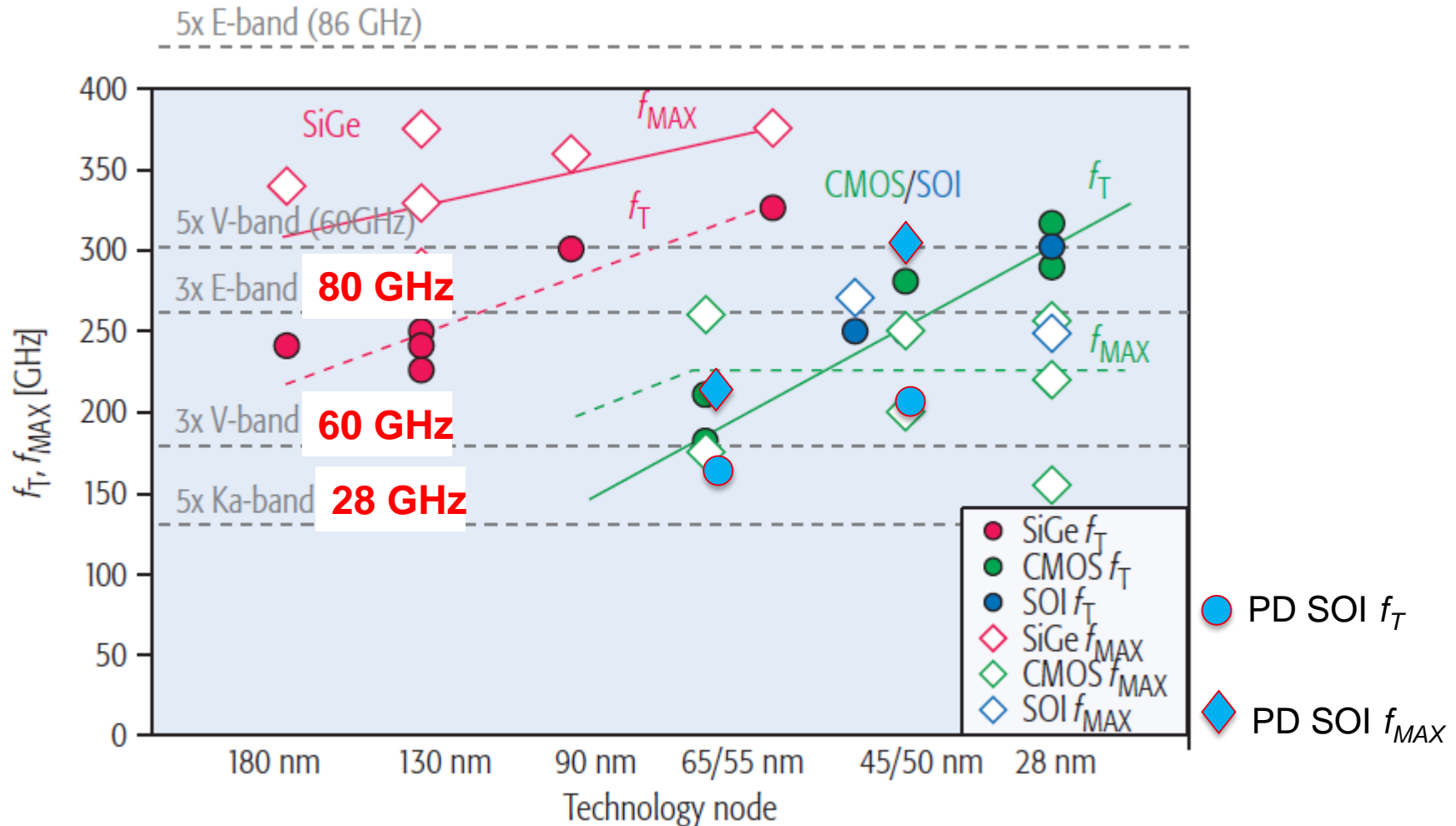
- Going to mmW frequencies at medium/long range:

- Requirement to attain an acceptable link budget: high antenna gain



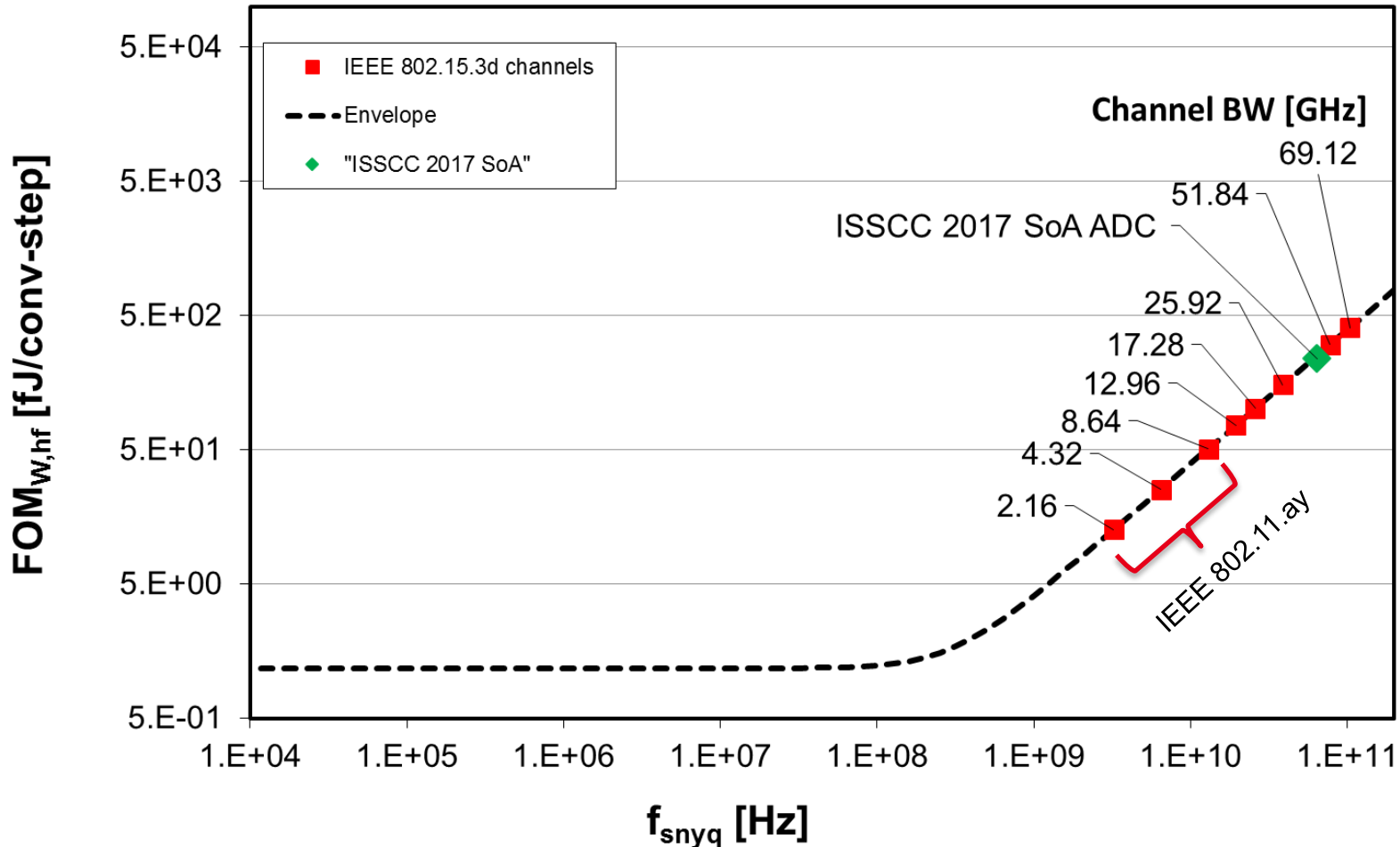
Antenna array systems for high gain

# HIGHER BW, HIGHER FREQUENCY: THE CHOICE OF TECHNOLOGY



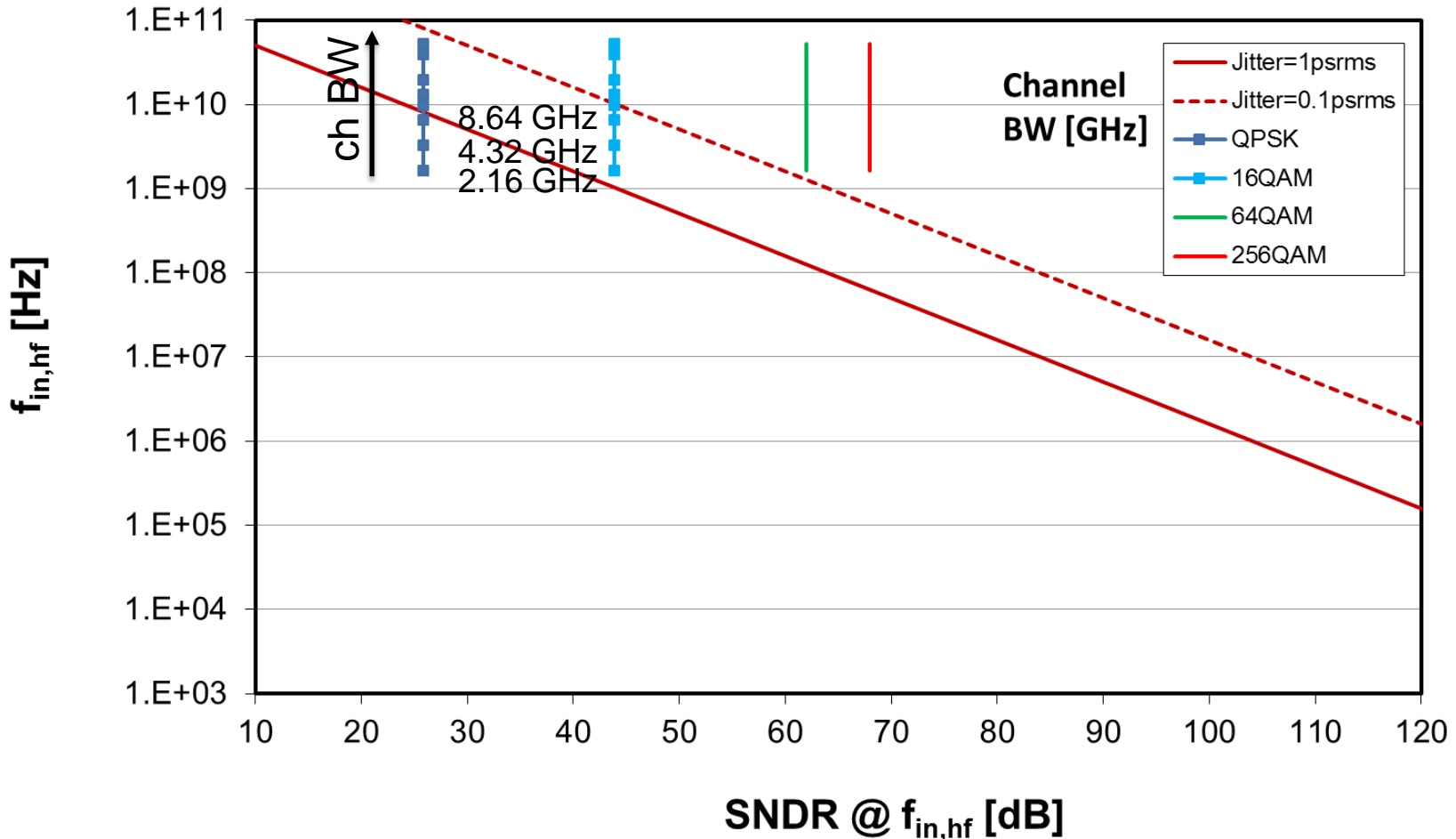
# HIGHER BW, HIGHER FREQUENCY: BASEBAND BW

The data converters bottle neck



# HIGHER BW, HIGHER FREQUENCY: BASEBAND BW

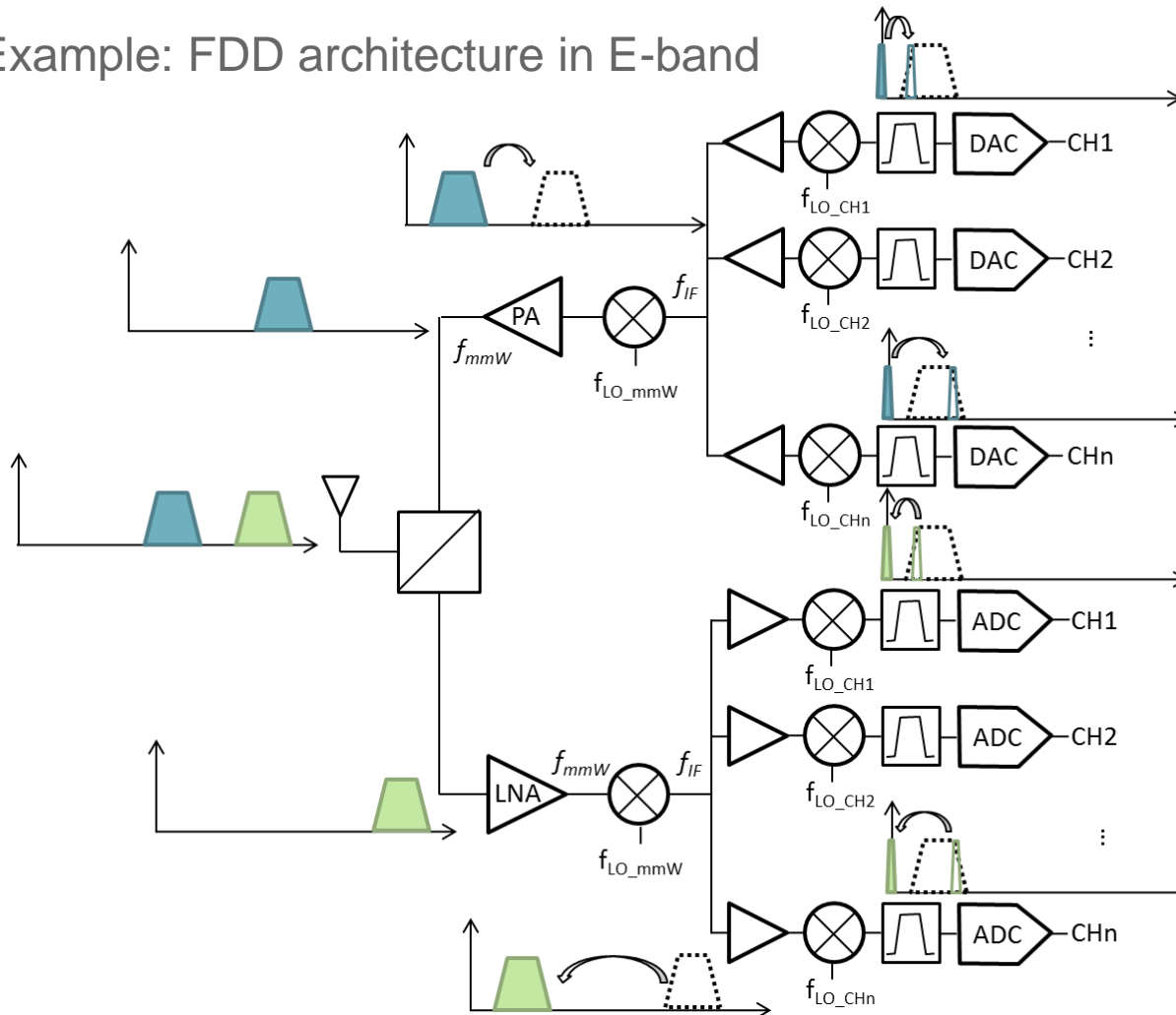
The data converters bottle neck



# HIGHER BW, HIGHER FREQUENCY: BASEBAND BW

The solution: channel bonding architectures

Example: FDD architecture in E-band



**Drawback:**  
several LOs are  
required  
simultaneously

# HIGHER MODULATION RATE: LO PHASE NOISE REQUIREMENT

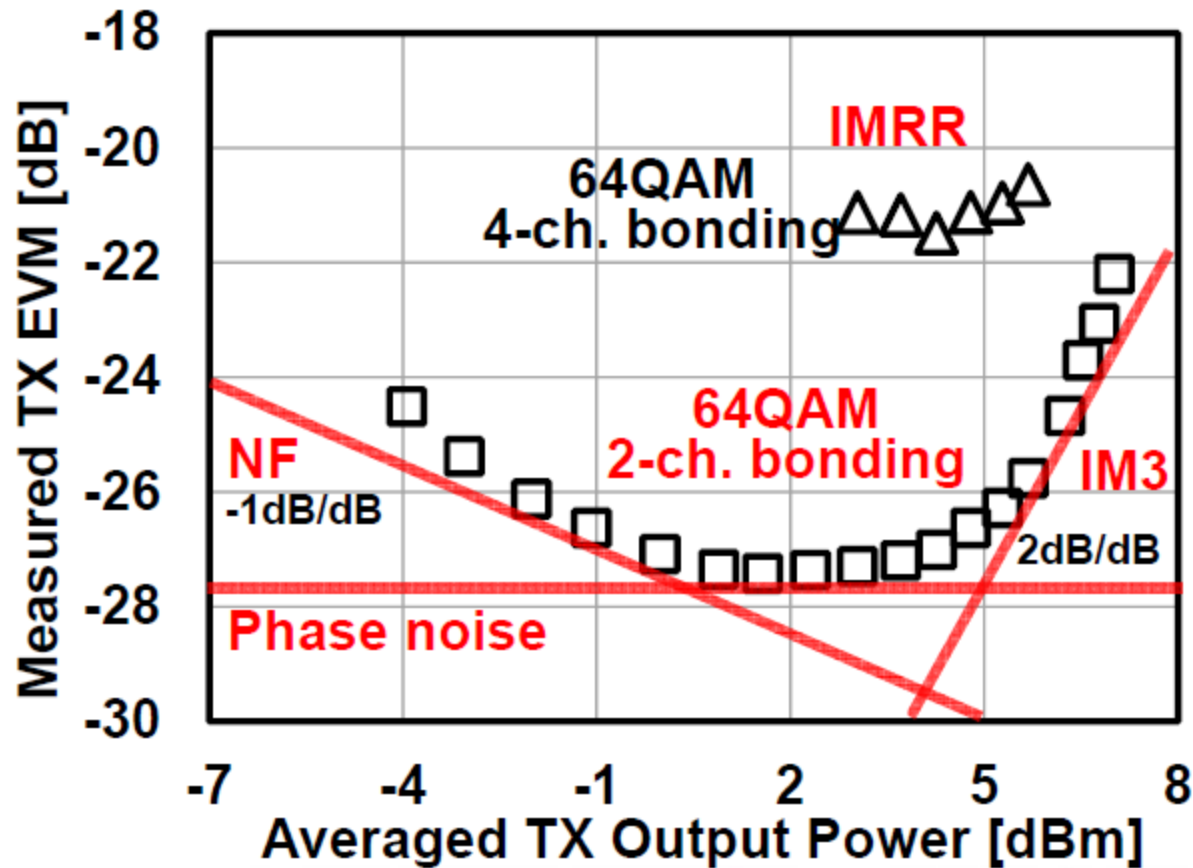
- Phase noise for 2% differentiator angle error (60 GHz SC, No DBB compensation)

Modulations	Phase Noise in dBc @ 1MHz
<b>QPSK</b>	<b>-100</b>
<b>16 QAM</b>	<b>-105</b>
<b>64 QAM</b>	<b>-114</b>
<b>128 QAM</b>	<b>-123</b>
<b>256 QAM</b>	<b>-132</b>
<b>512 QAM</b>	<b>-139</b>

- The constraint is higher at higher frequencies  $\sim 20\log(f)$
- Other degrees of freedom (Tx power, etc), are limited by phase noise effect

# HIGHER MODULATION RATE: LO PHASE NOISE REQUIREMENT

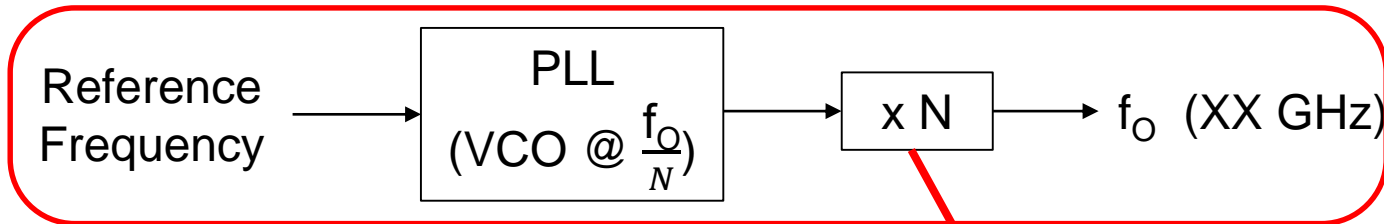
- EVM as a function of Tx power



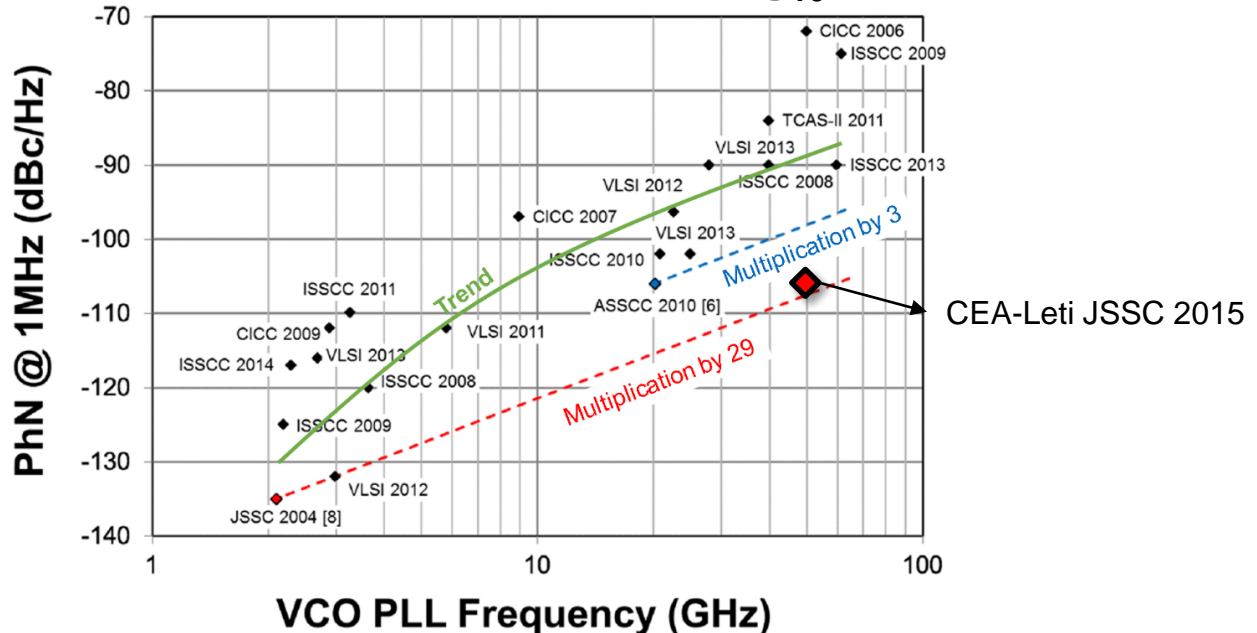


# HIGHER MODULATION RATE: LO PHASE NOISE REQUIREMENT

The solution: High order frequency multiplication



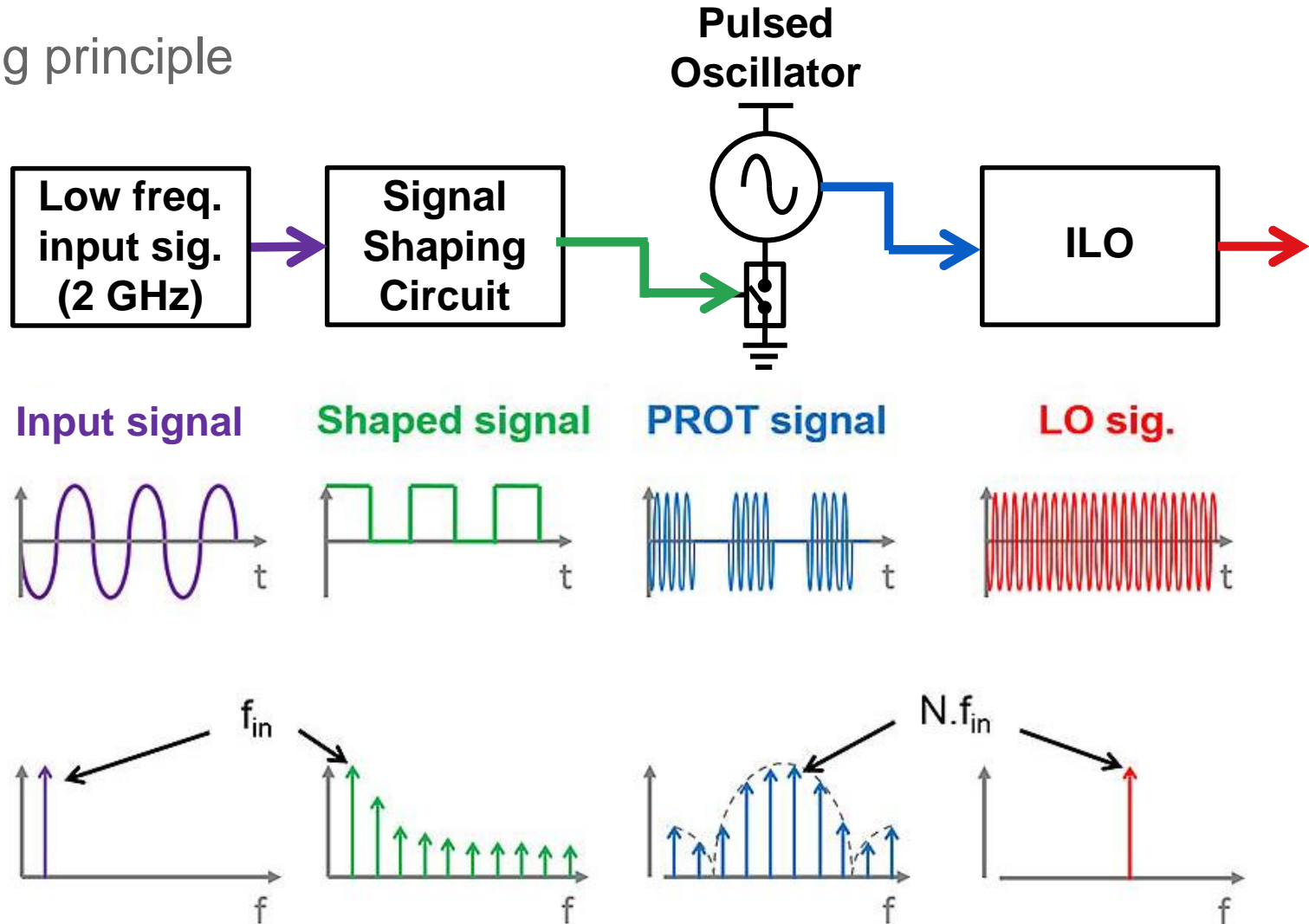
$$PhN(S_{f_0}) = PhN(S_{f_0/N}) + 20 \log_{10}(N)$$



# HIGHER MODULATION RATE: LO PHASE NOISE REQUIREMENT

High order frequency multiplication: the PROT technique

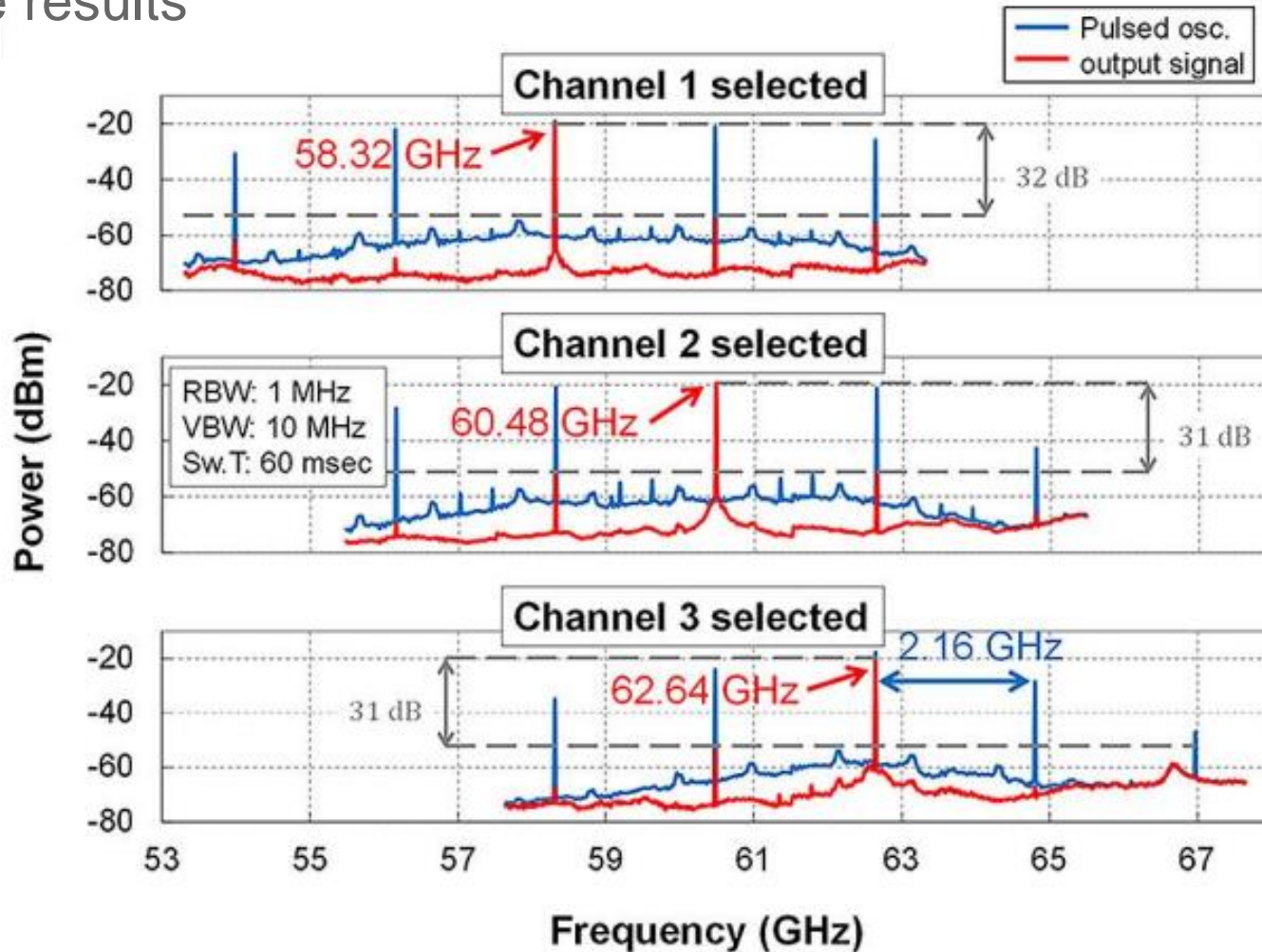
- Operating principle



# HIGHER MODULATION RATE: LO PHASE NOISE REQUIREMENT

High order frequency multiplication: the PROT technique

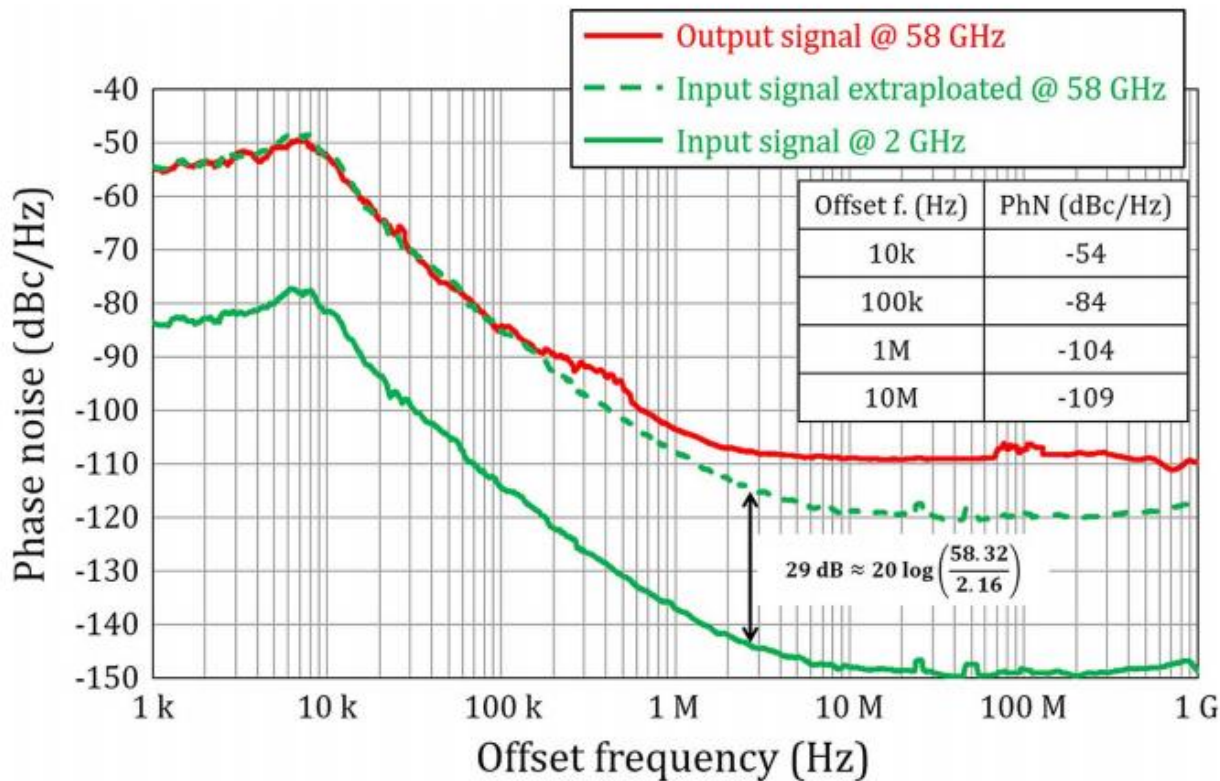
- Some results



# HIGHER MODULATION RATE: LO PHASE NOISE REQUIREMENT

High order frequency multiplication: the PROT technique

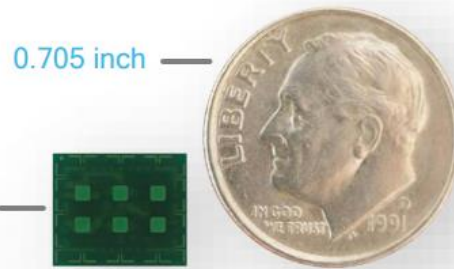
- Some results



# ANTENNA ARRAYS AT mmW

## Testing issues

60 GHz chipset commercial today for mobile devices

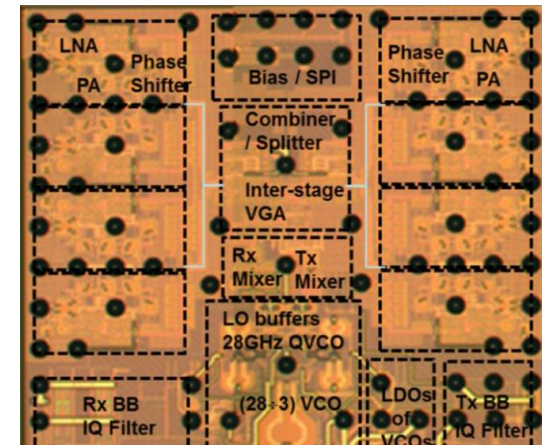
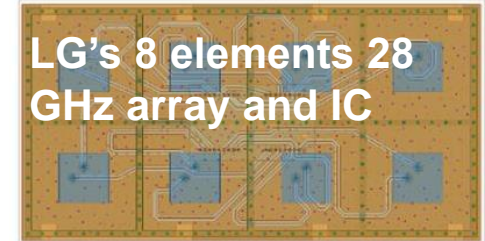


Qualcomm® VIVE™ 802.11ad technology with a 32-antenna array element

IBM and Ericsson 28 GHz 32 elements antenna array



LG's 8 elements 28 GHz array and IC

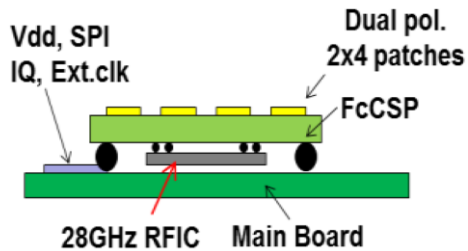


## The challenge of testing/calibrating phase-array systems:

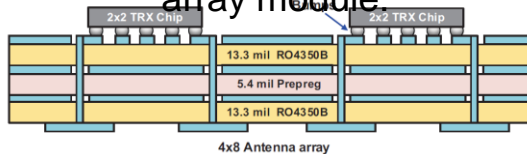
- Output RF nodes of Tx/Rx not available once antenna-array is build.
- Even if transceivers are previously tested, the assembly with the antennas has to be verified.

## Testing issues

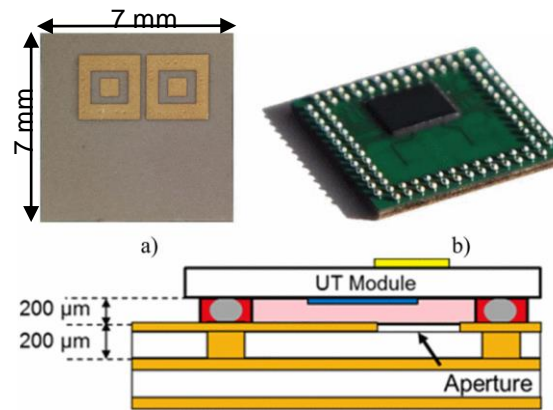
LG's 8 elements 28 GHz array module:



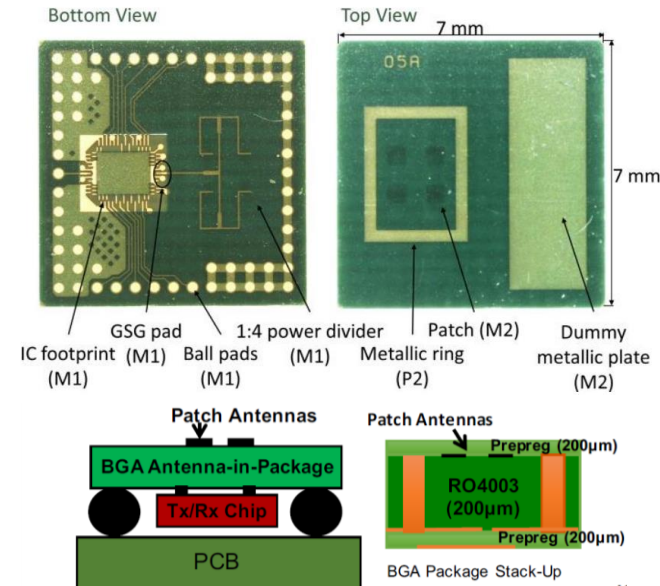
Broadcom 32 elements 28 GHz array module:



CEA Leti  
60 GHz array module:



STMicroelectronics 4 elements 130 GHz array module:



## mmW array modules use advanced packaging technologies

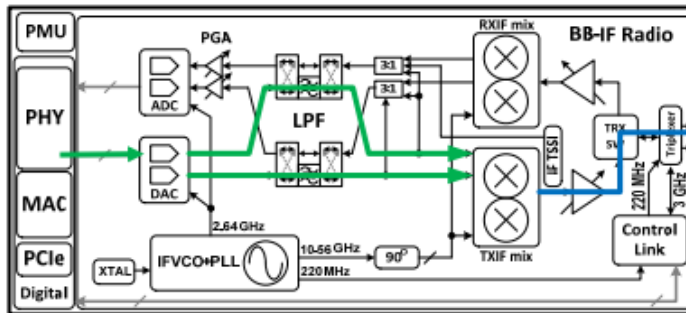
- Very sensitive to IC to antenna feed routing.
- Compact layout, difficult to access intermediate test point.

Testing issues

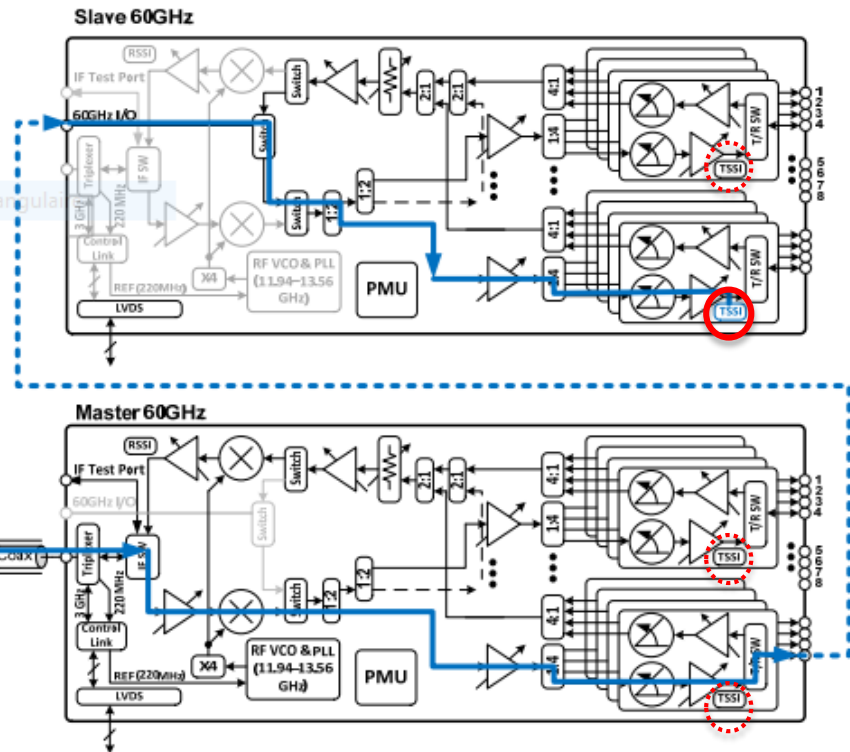
Example of calibration and BIST(i):

## Slave 60GHz: TX Calibration and RFBIST

- TX chain functionality: BB to PA output in Slave. Read back TSSI.
- Sweep BB tone. Read TSSI in Slave. **Calibrate 60GHz freq response and TX Pout** as fixed Back-Off from PSAT.



IE International Solid-State Conference

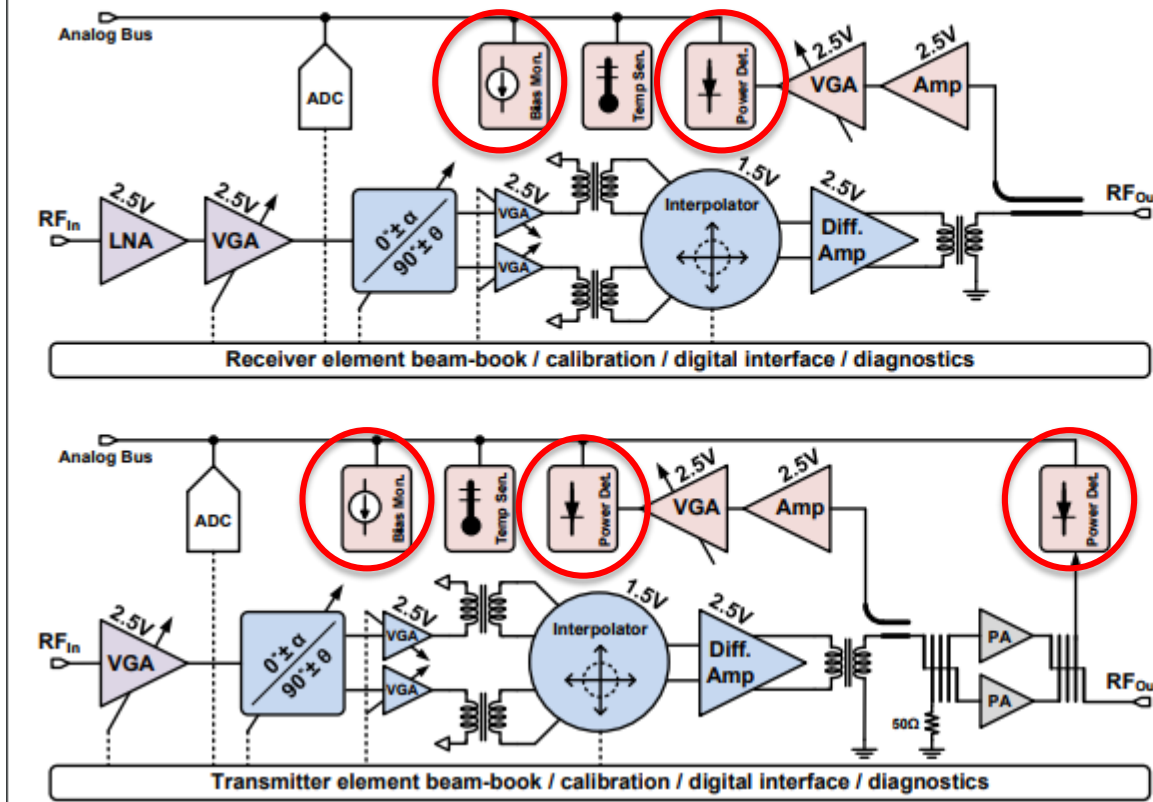


4.2: A 60GHz 144-Element Phased-Array Transceiver with 51dBm Maximum EIRP and  $\pm 60^\circ$  Beam Steering for Backhaul Application

Testing issues

Example of calibration and BIST(ii):

## RX/TX Phase Shifter Architecture



- 5-bit phase shifter with < 0.5 LSB phase error
  - NF < 7dB at 90GHz
  - Local beam and calibration memory
  - Built-in diagnostics and self test per element
- 5-bit phase shifter with < 0.5 LSB phase error
  - PSAT > 8dBm at 90GHz



# ANTENNA ARRAYS AT mmW

## Testing issues

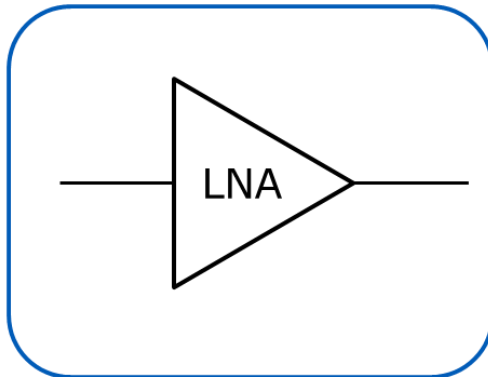
- **Two NON INVASIVE testing/calibration solutions:**
  - Alternate TEST
  - Using temperature as TEST OBSERVABLE

# ANTENNA ARRAYS AT mmW

## Testing issues

- Alternate TEST: operating principle

### ORIGINAL CIRCUIT



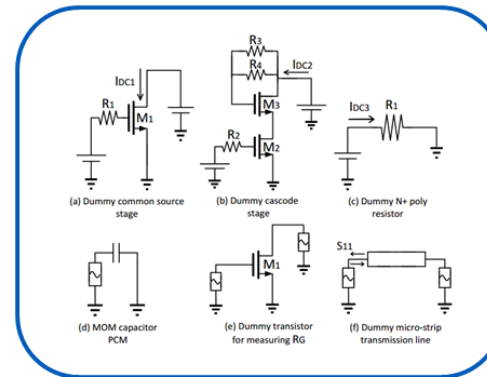
Performance metrics:

- $S_{21}$
- $S_{11}$
- $S_{22}$
- NF
- $IIP_3$

$P_j$

**Inference**

### ALTERNATE CIRCUITS



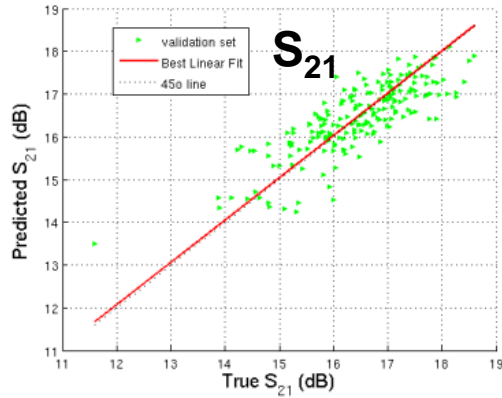
Measurement metrics:

- $I_{DC1}, I_{DC2}, I_{DC3}$
- $C_{MOM}$
- $R_G$
- $S_{11}$  TL

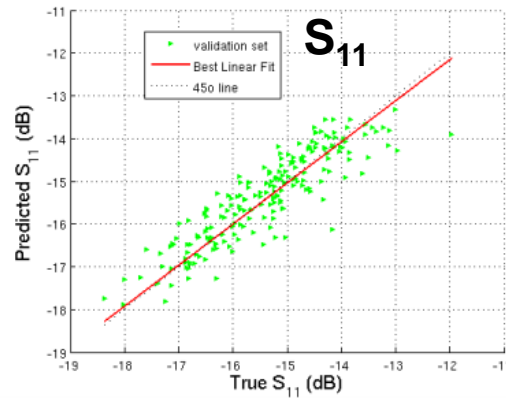
$X_i$

## Testing issues

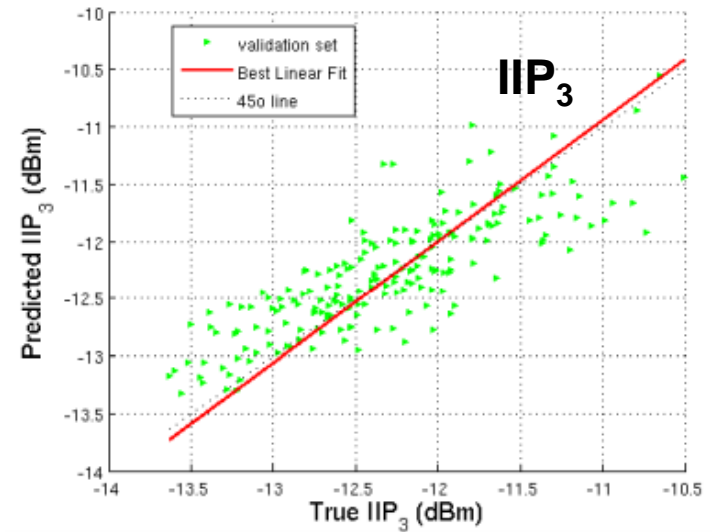
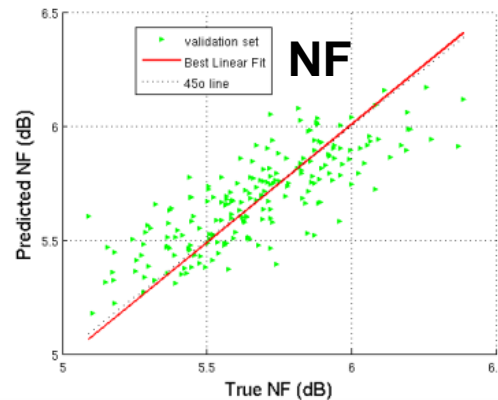
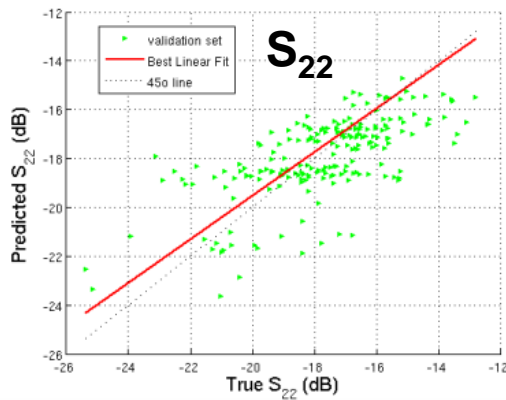
- **Alternate TEST: some results**



(a)  $S_{21}$



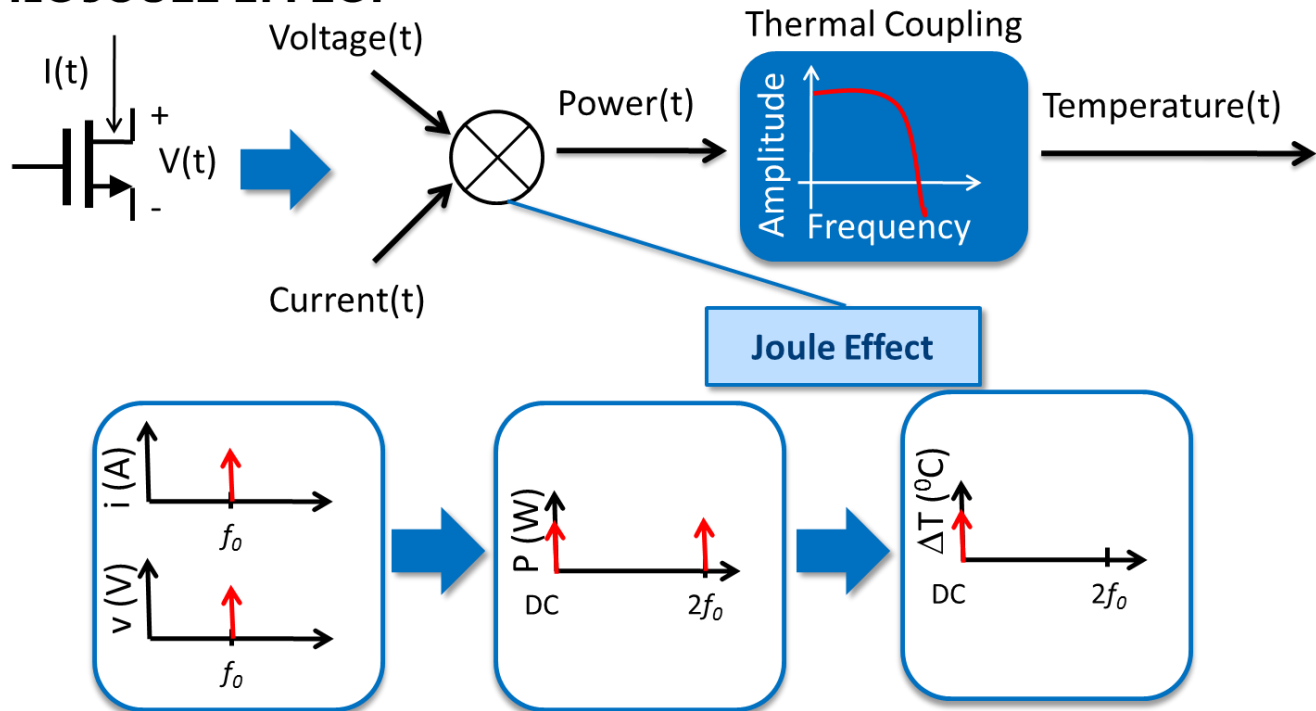
(d)  $S_{11}$



## Testing issues

- Thermal TEST: operating principle

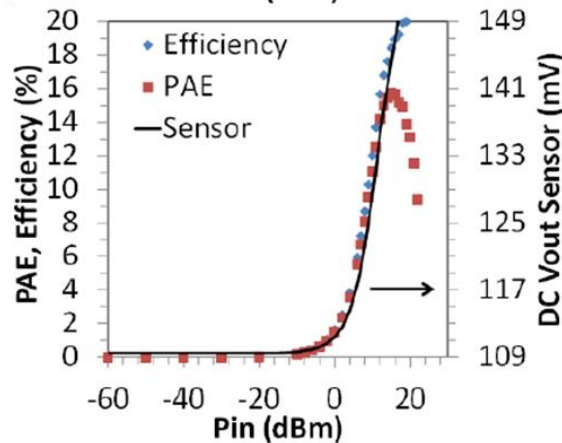
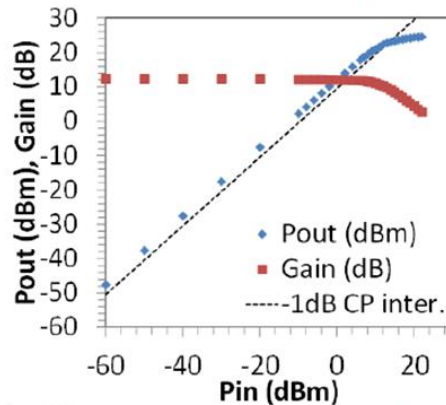
### □ DYNAMIC JOULE EFFECT



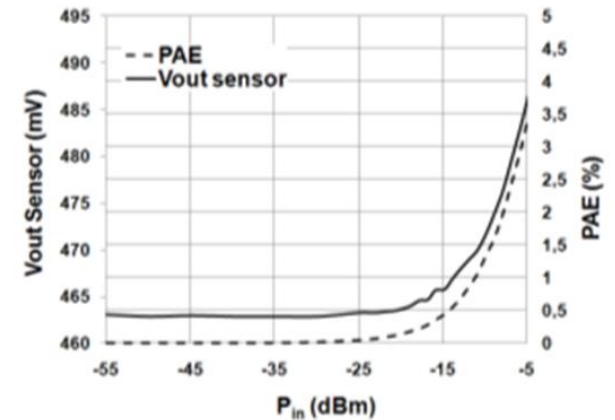
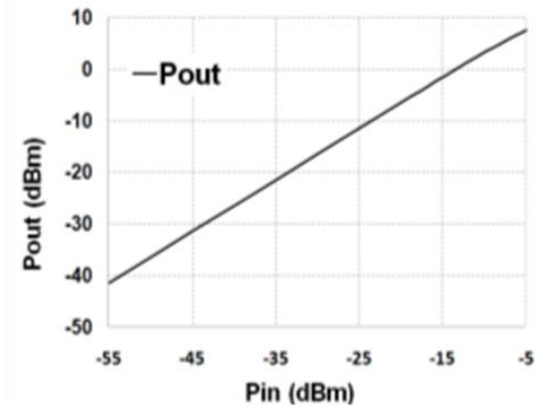
## Testing issues

- Thermal TEST: some results

- 1.9 GHz PA



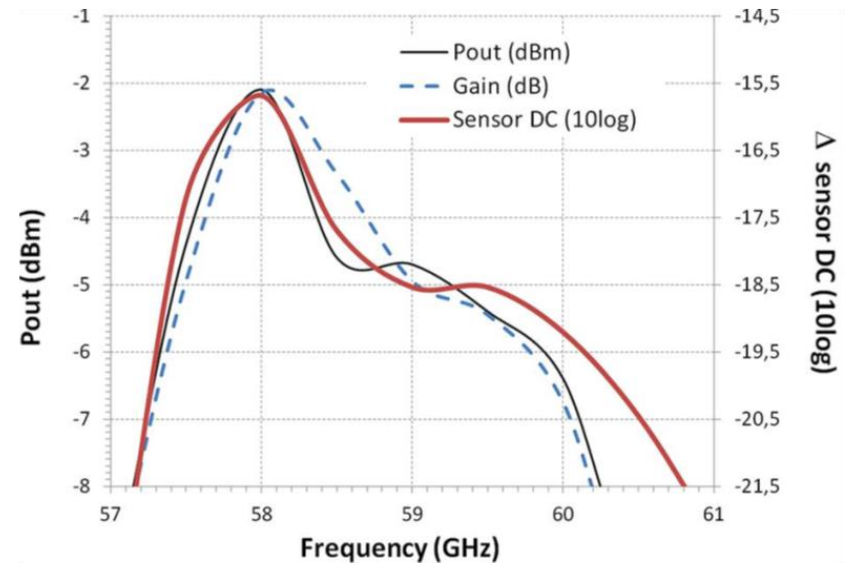
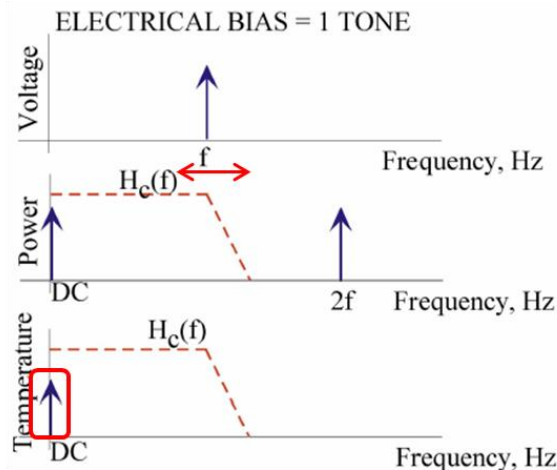
- 60 GHz PA



## Testing issues

- **Thermal TEST: some results**

- 60 GHz PA: Homodyne measurements of center frequency and BW



- Double correlated test: sensor output is measured with no input (dissipation due to bias). Relative changes in sensor output correspond to changes in input frequency for fixed  $P_{in} = -5$  dBm

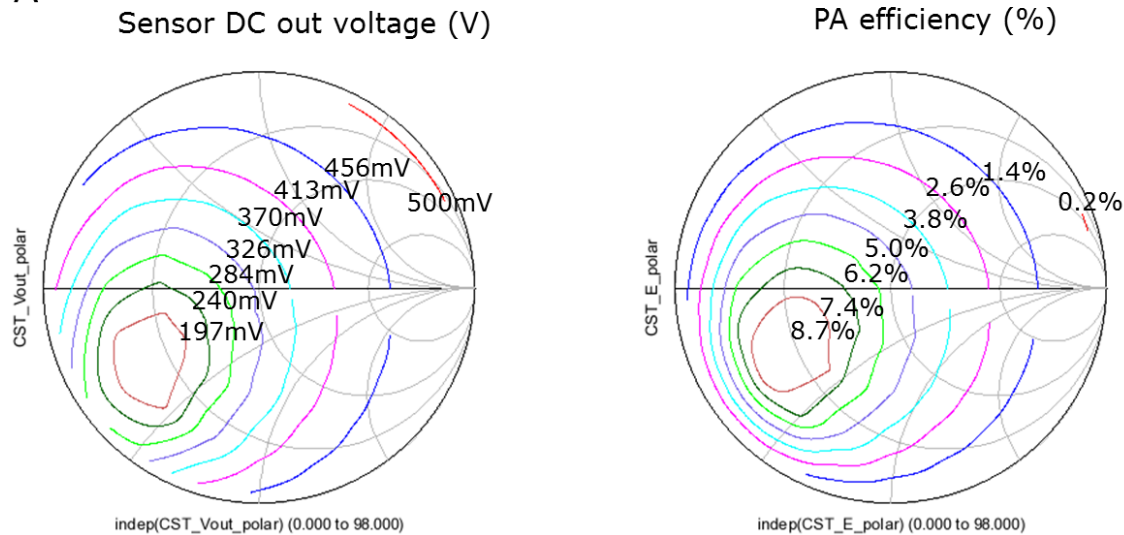
## Testing issues

- **Thermal TEST: some results**

- Results from thermal test can be used to check the IC to antenna assembly:

- Correlation between thermal sensor output and PA efficiency (class A-B) for load-pull test.

1.9 GHz PA



Simulation results: fixed input power, variable output load impedance.

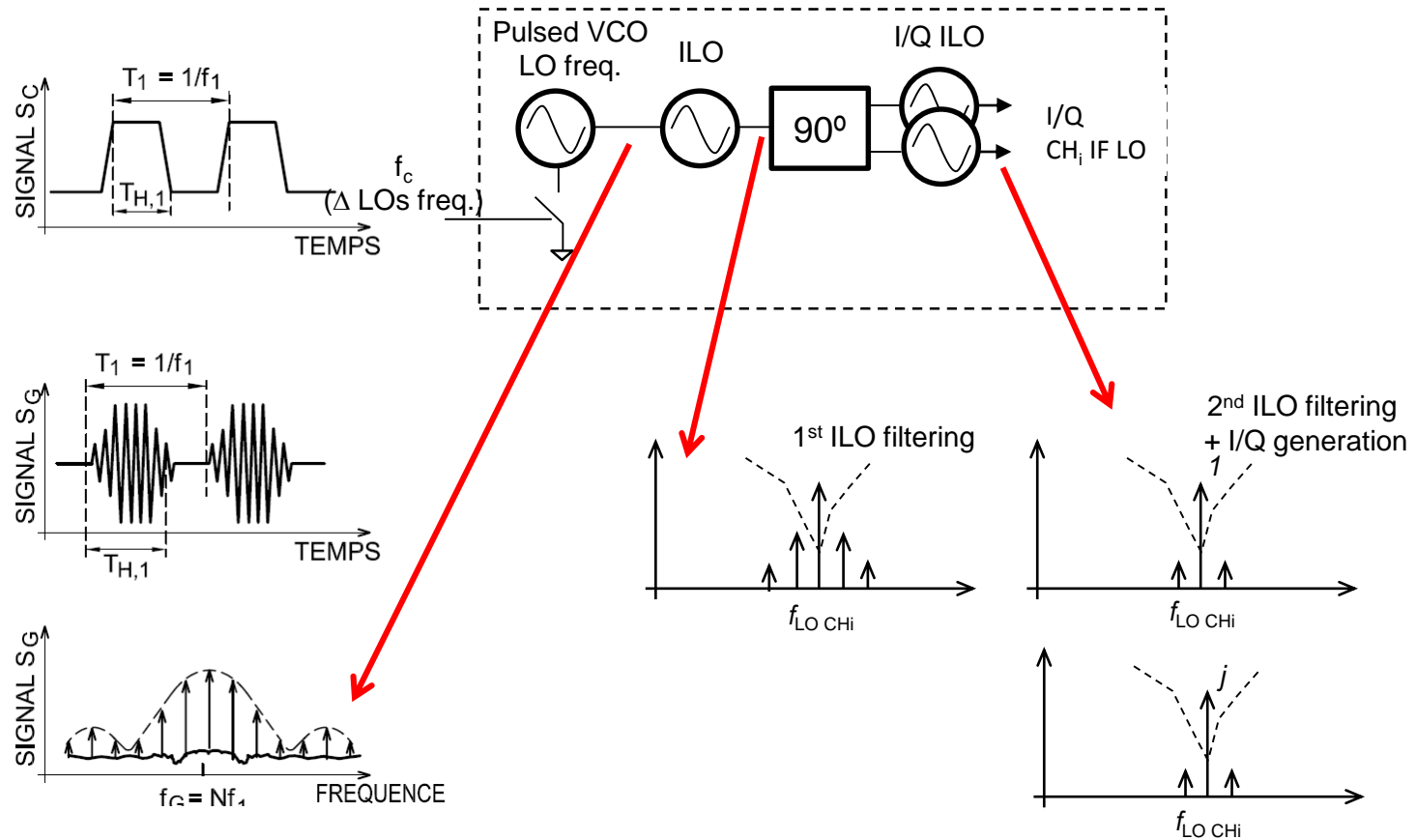
## CONCLUSIONS

- **Channel bonding architectures as a solution to implemented large-overall bandwidth transceivers at mmW frequencies (mainly for back-haul/front-haul applications)**
- **Innovative frequency synthesis techniques provide the required phase noise performance and flexibility required at mmW frequencies.**
- **Innovative testing and calibration techniques will be required for complex systems containing multi-antenna arrays.**



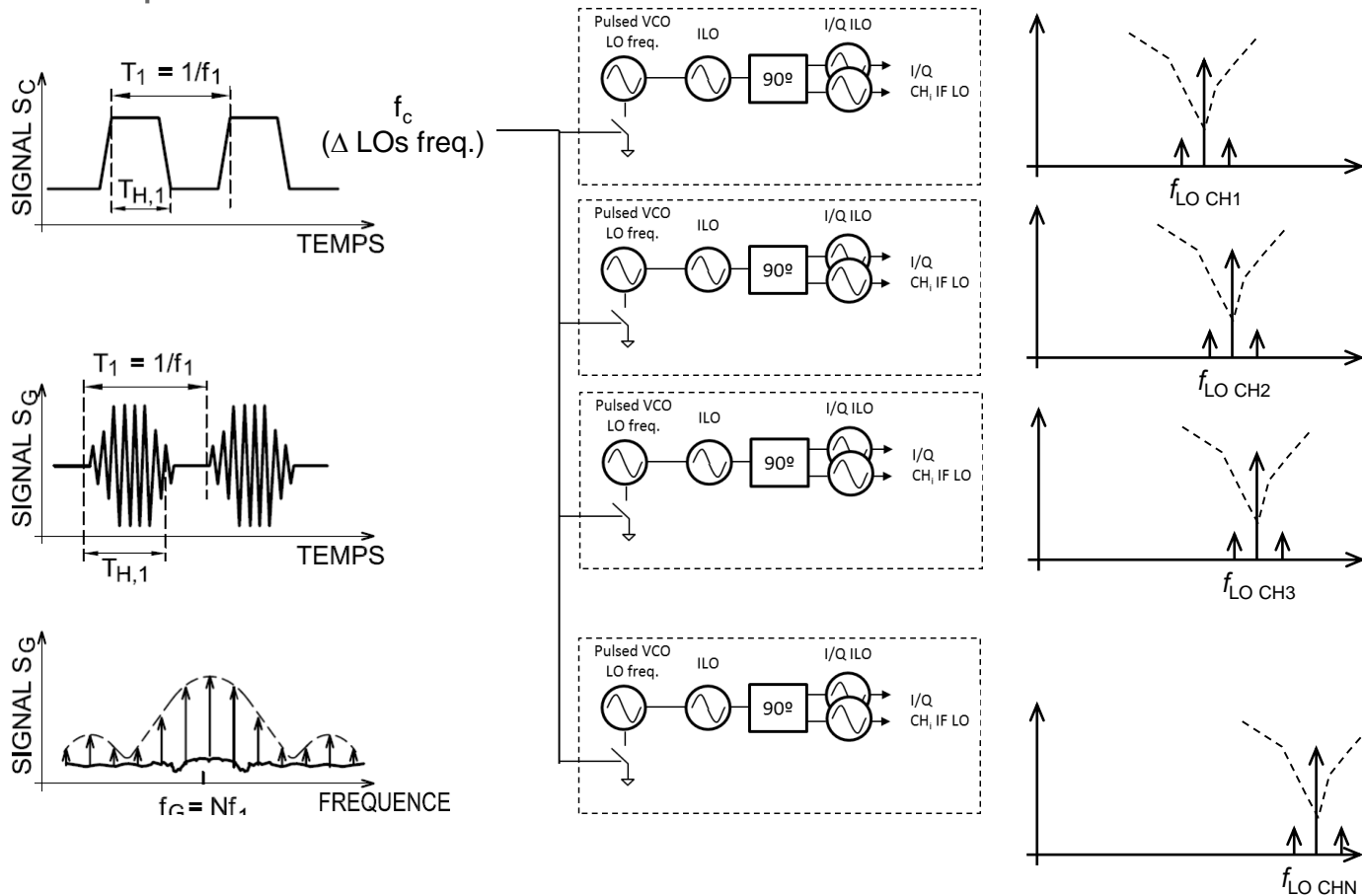
## Multi-LO generation technique

- Operating principle
  - Single channel:



## Multi-LO generation technique

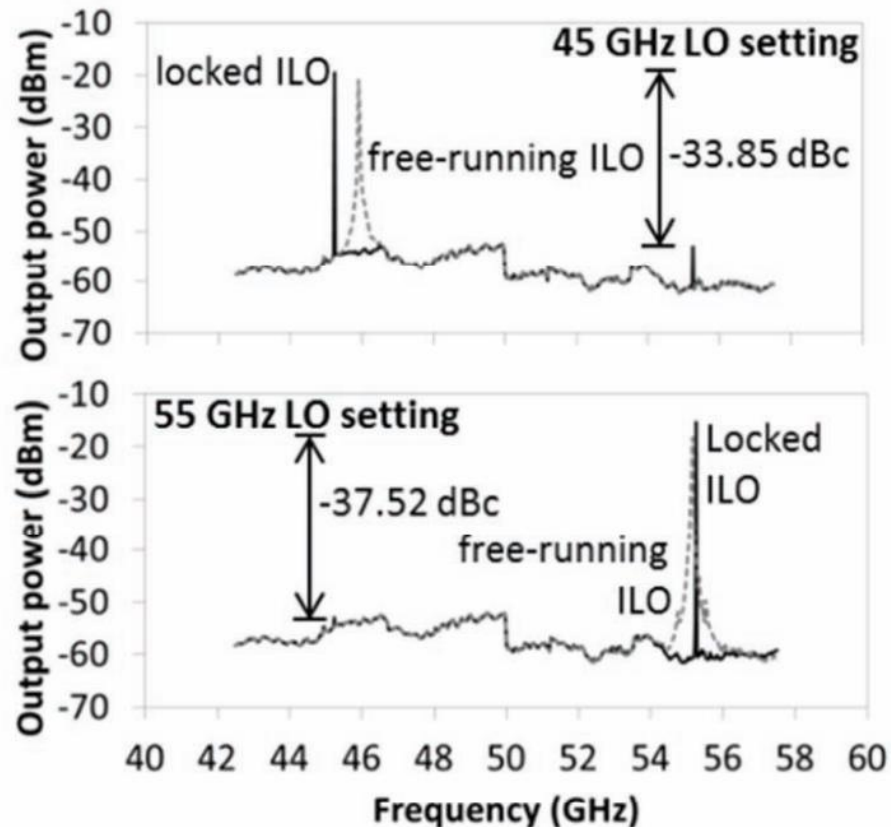
- Operating principle
  - Multiple channels



# CHANNEL BONDING ARCHITECTURES

## Multi-LO generation technique

- Results
  - Example for two channels



CH1: 45 GHz

CH2: 55 GHz